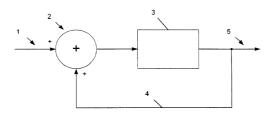
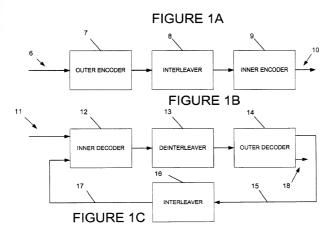
#### RATE N/N SYSTEMATIC. RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 11/24





## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 2/24

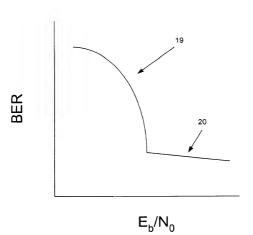


FIGURE 2

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 3/24

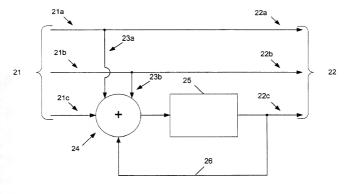


FIGURE 3A

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 4/24

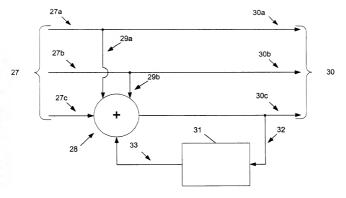


FIGURE 3B

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 5/24

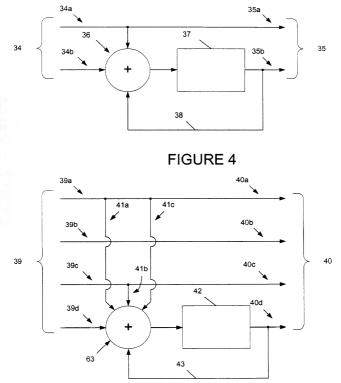
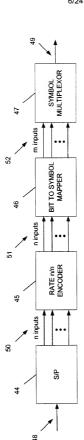


FIGURE 5

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 6/24



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## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 7/24

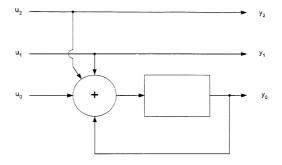


FIGURE 7A

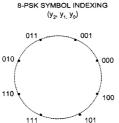
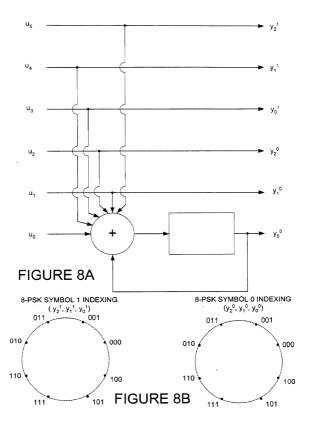
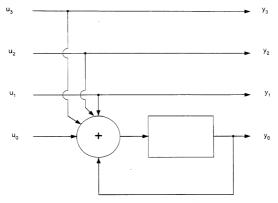


FIGURE 7B

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 8/24



## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidsoon, et al. Howrey Dkt. No. 01827.0037.CPUS01 9/24



#### FIGURE 9A

16-QAM symbol indexing (y<sub>3</sub>, y<sub>2</sub>, y<sub>1</sub>, y<sub>0</sub>)

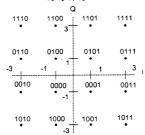


FIGURE 9B

# RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 10/24

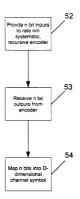


FIGURE 10

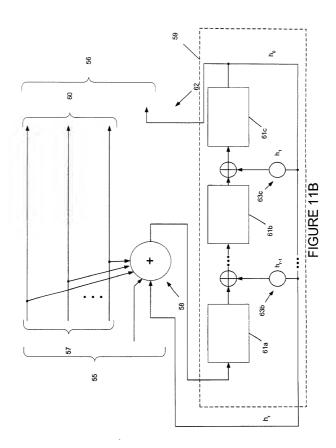
## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al.

Howrey Dkt. No. 01827.0037.CPUS01 11/24

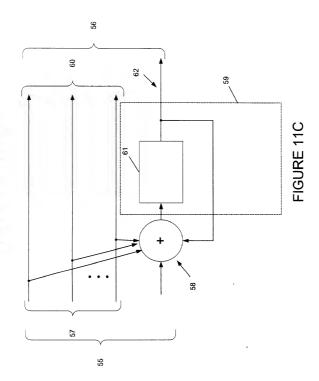
26 9 FIGURE 11A ້ ລ

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#### RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 12/24

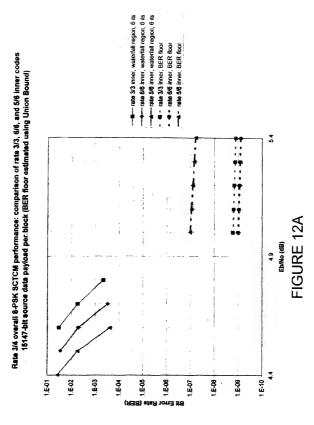


## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0937.CPUS01 13/24



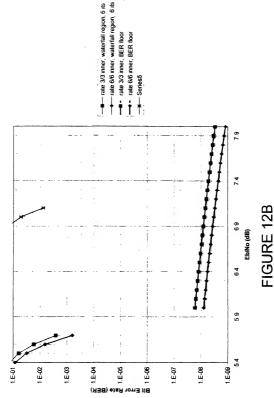
#### RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01





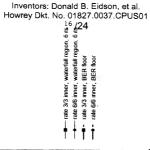
#### RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01





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Inventors: Donald B. Eidson, et al.



Rate 8/9 overall 8-PSK SCTCM performance: rate 3/3 inner code vs rate 6/6 inner code 15128-bit source data payload per block (BER floor estimated using Union Bound) 7.8 73 Eb/No (dB) 68 6.3 1.E-09 L (A38) ets 7 10113 118 11 11 11 118 12 118 118 118 1.E-01 1 E-02 1.E-03 1.E-07 1.E-08

FIGURE 12C

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Inventors: Donald B. Eidson, et al. However Donald B. Eidson, et al. However Donald B. Eidson, et al. However Donald B. Eidson 1272 uner, watertall region for the per property of the per Rate 4/5 overall QPSK SCCC performance: rate 2/2 inner code vs rate 4/4 inner code 16384-bit source data payload per block (BER floor estimated using Union Bound) 3.8 FIGURE 12D Eb/No (dB)

1.E-06

(A38) eta Rate (BER)

1.E-07

1.E-03 1.E-04 1.E-04

1 E-01

1.E-10

1.E-08

RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 18/24 - JPL's \*best of rate 5/6 m=1 uner \* rate 3/3 m=1 inner, rate 2/3 outer - rate 6/6 m-1 inner, rate 2/3 outer z - Baseline rate 5/6 inner Rate 2/3 Overall, 15144 source bits, 5 iterations Simulated (Waterfall) Performance FIGURE 13A Rate 2/3 Overall Waterfall Comparisons Eb/No (dB) 3.6 1.E-02

15

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RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 19/24 Baseline rate 3/3 inner - JPL rate 3/3 inner Rate 5/6 Overall (15147 source bits per block, 6 iterations) Simulated (Waterfall) Performance FIGURE 13B Rate 5/6 Overall Waterfall Comparisons

1.E-03

1.E-01

DARRYSY, DESIGN

# Rate 3/3 vs Rate 6/6 Codes

TOTUNO ZZAZABO

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 21/24

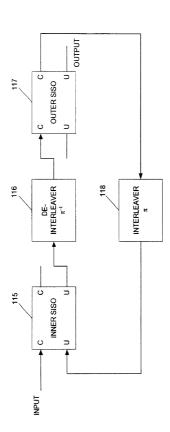


FIGURE 14

# RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howrey Dkt. No. 01827.0037.CPUS01 22/24

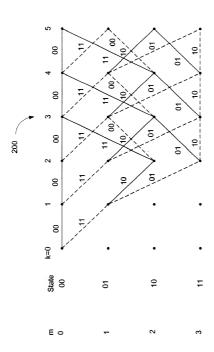


FIGURE 15

## RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al. Howeve Dkt. No. diazz 0037 CPUIS01

Howrey Dkt. No. 01827.0037.CPUS01

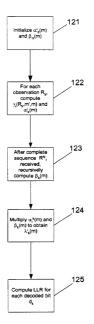


FIGURE 16

### RATE N/N SYSTEMATIC, RECURSIVE CONVOLUTIONAL ENCODER AND CORRESPONDING DECODER Inventors: Donald B. Eidson, et al.

Howrey Dkt. No. 01827.0037.CPUS01 24/24

